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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE  
(Case No. RA043D2DC)

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Technology Center 2100

In the Application of:

FARMWALD ET AL.

Serial No: 09/545,648

Filed: APRIL 10, 2000

Title: SYSTEM HAVING DOUBLE DATA  
TRANSFER RATE AND INTEGRATED  
CIRCUIT THEREFOR

Group  
Art Unit: 2181

Before  
Examiner: G. Auve'

Assistant Commissioner for Patents  
Washington, DC 20231

AMENDMENT

Dear Sir:

Kindly amend the application as follows:

IN THE CLAIMS:

Kindly Amend the following claims:

In claim 170, line 1, after circuit insert --device--.

In claim 171, line 1, delete "system" and substitute --integrated  
circuit device--.

In claim 179, line 1, delete "system" and substitute --integrated  
circuit device--.

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